THE MTL AND PARALLEL COMPUTING WORKSHOP

DATE: March 3-4, 2011
VENUE:國家高速網路與計算中心 **Classroom B**
30076 新竹市科學園區研發六路**7**號

AGENDA

Day 1	
Time	Issue
8:30-9:00	Registration
9:00-12:00	✧ Basics of log in to MTL, the tools and environment. ✧ Parallel programming with OpenMP ✧ Parallel programming with Threading Building Blocks
12:00-13:00	Lunch
13:00-15:00	Presentation of the Taiwan Course Curriculum team (II)
15:00-15:15	Coffee Break
15:15-17:15	Discussion with professors
Day 2	
9:00-12:00	✧ Walk-through of examples developed during 2010 for academic use. ✧ Overview of Parallel Studio XE (the linux collection of development/analysis tools for parallel computing) ✧ Overview of new techniques, for example Array Building Blocks
12:00-13:00	Lunch
13:00-15:00	Presentation of the Taiwan Course Curriculum team (II)
15:00-15:15	Coffee Break
15:15-17:15	Discussion with professors

Dr. Michael Wrinn

Michael Wrinn manages Intel's Innovative Software Education team, which collaborates with universities to bring parallel computing to the mainstream of undergraduate education. He also works with the ACM Education Council to bring industrial perspective to curriculum evolution. Prior Intel roles include managing Intel's software engineering lab in Shanghai, and directing research on human interface technology. He was Intel's representative for the original OpenMP specification, and remains active in the parallel computing community. Before joining Intel, Michael worked at Accelrys, implementing commercial and research simulation codes on a wide variety of parallel/HPC systems. He holds a B.Sc. and Ph.D. (in quantum mechanics) from McGill University.

Dr. Clay Breshears

Clay Breshears is the author of "The Art of Concurrency" published by O'Reilly in May 2009 and a self-admitted Thread Monkey. He has been with Intel since September 2000. He started as a Senior Parallel Application Engineer at the Intel Parallel Applications Center in Champaign, IL, implementing multithreaded and distributed solutions in customer applications. Clay is currently a Content Concierge for the Intel Developer Network Organization, specializing in multi-core and multithreaded programming and training. Clay received his Ph.D. in Computer Science from the University of Tennessee, in 1996, but has been involved with parallel computation and programming for over twenty-five years; six of those years were spent in academia at Eastern Washington University and The University of Southern Mississippi.

主辦單位：美商英特爾亞太科技有限公司、教育部「網路通訊人才培育先導型計畫」
協辦單位：國立清華大學、教育部「前瞻晶片系統設計人才培育先導計畫嵌入式軟體連盟(ESW)」